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9/18/01
01/00 #3

Docket No.: Amber.5739B

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Fitzgerald et al. **GROUP:** Unknown
SERIAL NO: 09/923,207 **EXAMINER:** Unknown
FILED: August 6, 2001
FOR: GATE TECHNOLOGY FOR STRAINED SURFACE
CHANNEL AND STRAINED BURIED CHANNEL
MOSFET DEVICES

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

INFORMATION DISCLOSURE STATEMENT

In compliance with 37 C.F.R. §§1.56, 1.97, and 1.98, Applicant submits
copies of the documents listed on the attached Form PTO-1449.

The Commissioner is authorized to charge Deposit Order Account No. 19-
0079 for any further fee that is required.

Respectfully submitted,

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited on
the date shown below in an envelope with sufficient postage addressed to the: Commissioner of Patents and
Trademarks, Washington, D.C. 20231, Box IDS.

Amy M. Flick

9-10-2001

Date